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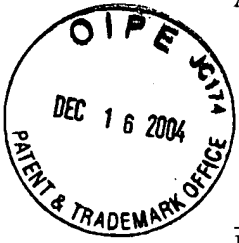
TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/647,217-Conf. #1901	
	Filing Date	August 26, 2003	
	First Named Inventor	Yoshitaka Kayukawa	
	Art Unit	2811	
	Examiner Name	Not Yet Assigned	
Total Number of Pages in This Submission		Attorney Docket Number	SON-2810

ENCLOSURES (Check all that apply)		
<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to TC
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<input checked="" type="checkbox"/> Preliminary Amendment	<input type="checkbox"/> Petition	<input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)
<input type="checkbox"/> After Final	<input type="checkbox"/> Petition to Convert to a Provisional Application	<input type="checkbox"/> Proprietary Information
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT			
Firm Name	RADER, FISHMAN & GRAUER PLLC		
Signature			
Printed name	Ronald P. Kananen		
Date	December 16, 2004	Reg. No.	24,104

Application No.: 10/647,217

Docket No.: SON-2810



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Yoshitaka Kayukawa et al.

Confirmation No.: 1901

Application No.: 10/647,217

Art Unit: 2811

Filed: August 26, 2003

Examiner: Violet McCoy

For: SEMICONDUCTOR INTEGRATED CIRCUIT
AND METHOD FOR TESTING SAME

FIRST PRELIMINARY AMENDMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Prior to examination on the merits, please amend the above-identified U.S. patent application as follows: